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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/629,592	07/30/2003	Toshiyuki Kasai	116743	6227
25944 7590 07/02/2007 OLIFF & BERRIDGE, PLC			EXAMINER .	
P.O. BOX 1992	28		PATEL NITIN	
ALEXANDRIA, VA 22320		•	ART UNIT	PAPER NUMBER
			2629	
			MAIL DATE	DELIVERY MODE
			07/02/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)				
	10/629,592	KASAI, TOSHIYUKI				
Office Action Summary	Examiner	Art Unit				
	Nitin Patel	2629				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	correspondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION B6(a). In no event, however, may a reply be tir rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 30 Ju	<u>ıly 2003</u> .	•				
· <u> </u>	,					
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under E	x parte Quayle, 1935 C.D. 11, 4	53 O.G. 213.				
Disposition of Claims						
4) Claim(s) 1-15 is/are pending in the application.	4)⊠ Claim(s) <u>1-15</u> is/are pending in the application.					
4a) Of the above claim(s) is/are withdraw	4a) Of the above claim(s) is/are withdrawn from consideration.					
5) Claim(s) is/are allowed.	5) Claim(s) is/are allowed.					
6)⊠ Claim(s) <u>1,4-7 and 9-15</u> is/are rejected.)⊠ Claim(s) <u>1,4-7 and 9-15</u> is/are rejected.					
•	Claim(s) <u>2,3 and 8</u> is/are objected to.					
8) Claim(s) are subject to restriction and/or	r election requirement.					
Application Papers						
9) The specification is objected to by the Examine	r.					
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.						
Applicant may not request that any objection to the	drawing(s) be held in abeyance. See	e 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the correct	· · · · · · · · · · · · · · · · · · ·	•				
11)☐ The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO-152.				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign	priority under 35 U.S.C. § 119(a)-(d) or (f).				
a)⊠ All b)□ Some * c)□ None of:						
1.⊠ Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents	s have been received in Applicati	on No				
3. Copies of the certified copies of the prior	ity documents have been receive	ed in this National Stage				
application from the International Bureau	• • • • • • • • • • • • • • • • • • • •					
* See the attached detailed Office action for a list	of the certified copies not receive	ed.				
Attachment(s)						
1) Notice of References Cited (PTO-892)	4) Interview Summary					
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date 10/28/2003. 	Paper No(s)/Mail D 5) Notice of Informal F 6) Other:					

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DETAILED ACTION

Claim Rejections - 35 USC § 112

1. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 14,15 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claims 14 and 15 states that electro optical device is mounted thereon, it is not clear mounted where or how, since claim 1 is an electronic circuit, it is not clear mounted how or where, correction required.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

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Claims 1,4-7,9-15 are rejected under 35 U.S.C. 102(e) as being anticipated by Fuse et al., (U.S. Patent No. 5,089,812).

As per claims 1,7 Fuse shows an electronic circuit, comprising: a shift circuit that shifts j-bit digital data (j is a natural number) to be converted into k-bit digital data (k is a natural number); and a correction circuit that is electrically coupled to the shift circuit, the correction circuit continuously changes the k-bit digital data that is obtained by the shift circuit in accordance with the change of the j-bit digital data (in col.4 lines 37-60 where 5-bit signal which is changed to 6-bit signal).

As per claims 4-6,9-15 Fuse teaches the correction circuit being an adder (read as element 23 a limiter and in fig.8, 9) and the shift circuit determining a number of bits by which the j-bit digital data is shifted in accordance with the value of the j-bit digital data (in fig.4 and 6).

Allowable Subject Matter

3. Claims 2-3,8 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin Patel whose telephone number is 571-272-7677. The examiner can normally be reached on 8:00-5:00.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin H. Shalwala can be reached on 571-272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Nitin Patel Primary Examiner Art Unit 2629

> NITIN I. PATEL PRIMARY EXAMINER